V.ARUN

Email Id: venarunxp@gmail.com

Mobile No: 9791966994

55A/2 Gandhi salai,

Rasipuram -637408.

Objective

Seeking a position to build a career in teaching at the institutions of higher learning and contribute to the growth of the institution and in turn realize my own growth.

Academic Chronicle

Academic Year	Course	Aggregate
2011	**M.E - VLSI DESIGN** K.S.Rangasamy College Of Technology	7.2 CGPA
2009	**B.E - Electronics and Communiction Engineering** K.S.R.College Of Engineering	63%
2004	**HIGHER SECONDARY SCHOOL EDUCATION** Government Higher Sec. School R.Puduppalayam	68%
2001	**SECONDARY SCHOOL OF EDUCATION** Government Higher Sec. School R.Puduppalayam	80.6%

Experience

Total Experience: 6 Years 11 Months

- Worked as Assistant Professor in the department of Electronics and Communication Engineering in **MUTHAYAMMAL ENGINEERING COLLEGE, Rasipuram**.

- Worked as an Assistant Professor in the department of Electronics and Communication Engineering in **K.S.R INSTITUTE FOR ENGINEERING AND TECHNOLOGY, Tiruchengode**.

- Currently working as an Assistant Professor in the department of Electronics and Communication Engineering in **JAYALAKSHMI INSTITUTE OF TECHNOLOGY, THOPPUR.**

Area of Interest

- VLSI Design
- Microprocessor and its applications
- Animation and multimedia

Technical Skills

- MATLAB
- ANSYS
- SYNOPSYS

Other packages: Unreal Engine 4.,Unity3d, Cinema4d, Blender.
Created a chess game using Unity3d

Papers Handled

- Microprocessors and Microcontrollers
- Communication Theory
- Digital Signal Processing
- Electromagnetic fields
- Transmission lines and Wave Guides
- Antennas and wave propagation
- RF & Microwave Engineering
- Electronics Circuits 1
- Electronics Circuits 2
- Linear Integrated Circuits
- Television and Video Engineering

Academic Participation

- Applied & Received a grant of **Rs.30000/-** from **ICMR** for Orgnizing two days National level Seminar on "Advanced Medical instruments for Robotic treatments" – Organising Secretary for the Year 2017 in **JAYALAKSHMI INSTITUTE OF TECHNOLOGY.**
- Guided a project which is selected as **one among 100 projects all over India** in *TEXAS Instruments Project Contest*
- Conducted two workshops on Basic Electronics and PCB Design.
- Established laboratories.
  1.VLSI Lab (10 lakhs worth)
  2.Microwave and Optical Lab (15 lakhs worth)
- Department Exam cell coordinator. (KSRIET)
- Class Advisor (2015-2017).
- Conducted a workshop on *"Tanner and Xilinx"*
- Organized 3 guest lectures for students on "*Communication systems & TLWG"* *Conducted by Dr.P.Dhananjeyan Prof/PEC*.
- Organized a FDP for faculties based on *"Antennas and Wave Propagation".*

Extracurricular Activities

- ISTE Life Time Member
- Student Member of IEEE

Professional Competencies

- Effective Communication
- Self-Motivation for achieving targets and multiple tasks
- Learning Ability for updating the latest technologies from time to time
- Smart worker

Journal Published

- Published a paper on **"INCREASING THE NETWORK LIFETIME IN CLUSTER USING MOBILE SINKS FOR WIRELESSBASED SENSOR NETWORKS"**iCARE-International Journal on Concurrent Applied Research in Engineering and Management Vol 1(4),April 2016,www.icarejournal.com.

Paper Presentation

- Presented a paper on "Modified Architecture for Viterbi Decoding Algorithm With VLSI implementation" in National conference, KSRCT, Tiruchengode on 1st April 2011.
- Presented a paper on "An Efficient Architecture for Error Control Coding using Viterbi Decoding Algorithm with VLSI Implementation" in National conference, Hindustan College of Engineering and Technology Coimbatore, on 18th March 2011.
- Presented a paper on "Error Control Coding using Viterbi Decoding Algorithm with VLSI implementation" in National conference, Velammal Engineering College, Chennai on 17th March 2011.
- Presented a paper on "DSP Based Speech Processing and DBMS based Voice Production" in National conference, Paavai College of Engineering, Namakkal on 2nd April 2009.

Effective Participation in Various Workshops / Faculty Development Programs

Workshop:

- Participated in national level workshop on "Antenna Design and Simulation using ANSYS" at K.S.R. College of Technology on 6thMarch 2017.
- Participated in national level workshop on "Analog Electronics" by IIT Kharagpur at K.S.R. College of Technology on 4-14thJune 2013.

- Participated in national level workshop on "Frontend and Backend ASIC Design using Synapsis EDA Tool"K.S.R. College of Technology on 1st& 2ndMarch 2013.
- Participated in national level workshop on "Reconfigurable Technology and its Applications" at NIT Trichy on 17thNovember 2011.

Faculty Development Programs:
- Participated in Faculty Development Programme on "Antenna and Wave Propagation Reconfigurable Technology and its Applications" at K.S.R. College of Technology from11th - 13thDec 2014.
- Participated in Faculty Development Programme on "VLSI Design" at Erode Sengunthar Engineering College on 3rd& 4thDec 2013.
- Participated in Faculty Development Programme on "Programme on Signal and Image Analysis using Time/ Frequency Transforms" at Muthayammal Engineering Collegefrom30thMay - 12thJune 2011.

Project Details

PG Project

Project Title: Error Control Coding using Viterbi Decoding Algorithm with VLSI Implementations

Project Description: The internal Architecture of Viterbi decoder was modified and its functionality was verified with Synopsys EDA tool. Similarly its area also optimized and verified using Design Compiler Tool.

UG Project

Project Title:DSP Based Speech Processing and DBMS based Voice Production

Project Description: The objective of our project to design a Text to speech engine in Tamil Language. It was programmed in MATLAB.The Text Input are Converted as Voice/Speech automatically with the help of Microsoft SAPI.

Projects guided – 15

PG Projects Guided - 3

TI Project(*Shortlisted for Quarter Finals TI Contest*)

Project Title: Automated Patient Health Checking System

Project Description: This is an automated patient Health checking system Using TI MSP430 Kit. Programmed in Energia.It Helps us to Monitor Height, weight, BP and heart Beat.

Personal Profile

Name	V.Arun
Father's Name	S.Venkatesan
Date of Birth	21.11.1985
Nationality	Indian
Marital Status	Married
Spouse Name	S.Ramya
Languages Known	English, Tamil(to speak, read and write)

References

1. Dr. R. Govindaraju,

   Dean academic,

   VSA Group of Institutions,

   Ariyanur.

   (Ex-Director K.S.R. Group of Institutions)

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Declaration

I hereby declare that all the above furnished details are true to the best of my knowledge.

Place:Rasipuram   **Yours sincerely,**

Date: 1.5.2019

                                                       (V.ARUN)