

KARPAGAMBALS

Assistant Professor

Department of Electronics & Communication Engineering

Dhirajlal Gandhi College of Technology

Opp. To Salem Airport

Salem – 636 309.

EDUCATION

Master of Engineering in VLSI Design (CGPA: 92)

Sona College of Technology, Salem, India.

Bachelor of Engineering, Electronics and Communication Engineering (CGPA: 81.3)

2009-13 at Narasu's Sarathy Institute of Technology, Omalur, Salem.

Higher Secondary School leaving certificate (86.6% - State Board)

2009 at Sri Gayathri HR. Sec School, Salem.

Secondary School Leaving Certificate (77.6% - State Board)

2007 in Sri Saradha Vidyalaya HR. Sec School, Salem.

Professional Work Experience (1.8 years):

- Working as **Assistant Professor in the department of Electronics and Communication Engineering**, Dhirajlal Gandhi College of Technology, Salem from 4th June 2015 to till date.
 - Subjects handled:
 - Linear Integrated circuits and its applications (EE 6303)
 - Advanced radiation system VLSI design (CU 7101)
 - Advanced Computer Architecture (EC 6009)
 - High Performance (NC 7101)
 - Electronic Devices (EC 6201)
 - Communication Engineering (EC 6651)
 - Laboratories handled:
 - Linear and digital Integrated Circuits Laboratory (EE 6311)
 - Microprocessor and Microcontroller Laboratory (EC 6513 & CS 6412)
 - Circuits and Simulation Integrated Laboratory (EC 6411)
 - Computer Networks Laboratory (EC 2356)

Responsibilities held:

- **Class advisor (PG :2015-16, UG:2016-17)**
- **Newsletter and Annual report coordinator** for the Department of Electronics and Communication Engineering, Dhirajlal Gandhi College of Technology
- **Staff achievements details coordinator** (2016-17)
- **Students achievements details co-coordinator** (2015-16)
- **Technical training member** for placements in core companies in the Department of Electronics and Communication Engineering, Dhirajlal Gandhi College of Technology

PROJECTS

PG PROJECT : Analysis Of Digitally Controlled Delay Loop NAND Gate For Glitch-Free Design.
UG PROJECT: Performance Analysis Of Routing Protocols In Wireless Sensor Network.

AWARDS/ACHIEVEMENTS

- Received award of appreciation for **excellent academic performance** in PG(M.E-VLSI design)
- Participated and presented a technical paper titled **“Performance Analysis Of Routing Protocols In Wireless Sensor Network”** in the **MKCE-CONFLUENCE’13** held at **M.Kumarasamy College Of Engineering, Karur.**
- Participated in the event **“Paper Presentation”** in the National Level Technical Symposium (**LAKSHYA’12**) held at **KGiSL Institute of Technology, Coimbatore.**
- Participated in the event **“Paper Presentation”** in the National Level Technical Symposium (**iComET’14**) held at **Sona College Of Technology, Salem.**

Workshops\ Trainings participated:

- Participated in International workshop on **“Scholarly Research Publications: writing, citations and plagiarism(RPCP-16)”** held at Periyar University, Salem on February 2016
- Participated in three days **FDP** on **“Embedded systems”** held at Velalar college of Engineering and Technology from 16th to 18th December 2015
- Participated in the two days National workshop on **“Signal and Image Processing”** at Sona College of technology on 4th and 5th December 2015
- Attended Two days workshop on **“Simulation & Analysis of Electronics Circuits using EDA”** held on 28TH & 29TH FEB 2015 at Kongu Engineering College.
- Attended **“One day national workshop on CIRCUIT SIMULATION & PCB DESIGN USING ORCAD”** held on 2^{8TH} FEB 2011 at Anna University of Technology, Coimbatore
- Attended **“One day workshop on MICROWIND”** organized by the department of ECE – PG held on 24th SEP 2014 at Sona College Of Technology, Salem.

Journal Publication:

- Published paper in **International Journal of Recent and Innovation Trends in Computing and Communication** entitled **“Analysis of Digitally Controlled Delay Loop NAND Gate for Glitch Free Design”**

International Conference :

- Presented paper entitled **“Artificial Neural Network based character recognition approach for tamil characters”** in IEEE sponsored *International conference on innovations in information, Embedded and Computing systems(ICIECS’16)* at Karpagam college of Engineering, Coimbatore on 17th and 18th march 2016
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National Conference:

- Presented paper in *1st National conference on Emerging trends in communication systems (NCETCS-2016)* on the title “**Virtual Ground and Transmission gate based SRAM cell design for improved stability in ROM embedded SRAM**”, organized by Dhirajlal Gandhi College of Technology on 2nd April 2016

SKILL SETS

Design Tools	Xilinx ISE, Modelsim, Multisim, MICROWIND tool and DSCH.
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Languages	VHDL and Verilog.
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Industry Visits:

- One day visit to work floor of **Megawin Switchgear Private Limited**, Salem

TRAINING

- Undergone in-plant training in the area of “**Centralized Electrical Maintenance**” in the **Salem Steel Plant** during the period from 03/12/2010 to 07/12/2010.
- Undergone in-plant training in the area of “**Fundamentals Of Telecommunications**” in the **BSNL Salem** from 30/05/2011 to 03/06/2011.

EXTRA CURRICULAR ACTIVITIES

- Attended the district level Tamil essay writing competition and won 2nd place at Salem 2008.
 - Actively Participated in **NSS Special Camping Programme** on the theme “**HEALTHY YOUTH FOR HEALTHY INDIA**” organized at poosaripatty village/slum from 18/02/2011 to 24/02/2011.
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