

B.PRASAD M.E.

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OBJECTIVE

To utilize my knowledge and skills in imparting quality education to engineering students community and update myself with the technological advancements.

EDUCATION

MASTER OF ENGINEERING, (APPLIED ELECTRONICS) K.S.Rangasamy College of Technology, Tiruchengode.	8.6 CGPA [Distinction with Gold Medal]
BACHELOR OF ENGINEERING, (ELECTRONICS AND COMMUNICATION ENGINEERING.) Government College of Engineering, Salem.	68%
DIPLOMA (ELECTRONICS AND COMMUNICATION ENGINEERING.) C.S.I. Polytechnic College, Salem.	77% [Honors]
SECONDARY SCHOOL EDUCATION Sri Ramakrishna Sarada Hr. Sec. School, Salem.	73%

TECHNICAL SKILLS

- PCB Designing [OrCAD, PCB 123].
- Simulation [MultiSim, NetSim].
- Knowledge in Components and their packages.
- Assembling and testing of PCBs.

AREAS OF INTEREST

- Analog Electronics.
- PCB Designing.
- Image Processing.

TEACHING EXPERIENCE: 6 Years

- **ASSISTANT PROFESSOR – May 2012 to Till Date**

COLLEGE	K.S.RANGASAMY COLLEGE OF TECHNOLOGY, TIRUCHENGODE
SUBJECT TAUGHT	Electron Devices, Electronic Devices & Circuits, Electronic Circuits, Basics of Electronics Engineering, Linear Integrated Circuits, Computer Networks and Optical Communication.
LABS HANDLED	Electronics Circuit & Digital, Linear Integrated Circuits, Electronic Devices, Electronic Circuits and Simulation, Computer Networks, Microprocessor & Microcontroller and Optical Communication.
OTHER ACTIVITIES	<ul style="list-style-type: none"> • Course Coordinator for the Module Analog Electronics. • Technical Team Member for conducting Interview for Final year students. • Preparation of Question & Answer Template based on Bloom's Taxonomy. • Carried out works for NBA Tier I format. • Works connected to Department & COE. • Class Advisor for year 2013 – 2017.

- **ASSISTANT PROFESSOR – December 2011 to April 2012 [4 months]**

- **LECTURER - July 2010 to November 2011 [1 year 5 months]**

COLLEGE	SONA COLLEGE OF TECHNOLOGY, SALEM
SUBJECT TAUGHT	Electronics Circuit I, Embedded Systems & Electronics and Microprocessor, Digital Communication.
LABS HANDLED	Electronics Circuit I , Linear Integrated Circuits & PCB
CONTINUING EDUCATION CENTER	<p>Worked in SONAPCT [Center for PCB Design & Fabrication]</p> <ul style="list-style-type: none"> • Trained nearly 150 students. [Generated a Revenue of nearly Rs.1,00,000] • Consultancy work carried out for SONASPEED [Research Center working for ISRO].
OTHER ACTIVITIES	<ul style="list-style-type: none"> • Department Placement Coordinator for the year 2011 – 2012.

▪ **PROJECT ENGINEER - October 2006 – June 2010 [3 years 8 months]**

COLLEGE	SONA COLLEGE OF TECHNOLOGY, SALEM
NATURE OF JOB	Handled Tutorial Classes for Electronic Circuits I and Digital Electronics.
	Handled Digital Electronics Lab, Engineering Practices Lab, Electronic Circuits I lab and Linear Integrated Circuits Lab.
	Involvement in following Projects. <ul style="list-style-type: none"> ▪ Motor Automation for Bore well to Overhead Tank. ▪ Motor Automation for both Sump & Overhead Tank.
	Maintenance and Trouble Shooting of Electronic Equipments.
	Conducted Short Term Course on “PCB Designing & Fabrication”.
	ISO 9001:2000 Internal Auditor.

▪ **APPRENTICE - August 97 – July 98/August 98 – January 99 [1 year 4 months]**

COMPANY	RELCO PRIVATE LIMITED, SALEM & TALEMA ELECTRONICS, SALEM
NATURE OF JOB	Testing the quality of the incoming materials.
	Testing Single Line Resistor, Chip Resistor, Printed Circuit Transformer, Chokes.
	Maintaining Test Report for SLR & Chip resistor.

AWARDS / ACHIEVEMENTS

- Produced 100% result in Electronic Circuits I, Embedded Systems & Electronic and Microprocessor.
- Obtained 100% feedback for the subjects handled.
- Conducted Mock Interview & Group Discussion as a part of Placement activities.
- Acted as jury for project presentation and paper presentation for SAXON 12 [IEEE - National Level Technical Symposium] & for IVENOR 12 [IETE - National Level Technical Symposium].
- Organized and effectively participated in conducting workshops and conferences.
- Secured first rank in college level in M.E.

WORKSHOPS/ SEMINARS/ TRAINING/ CERTIFICATION

- Five days workshop on “Linear Integrated Circuits” conducted by Equip Foundation and K.S.Rangasamy Institute for Engineering & Technology.
- Five days workshop on “Electronic Circuits I” conducted by Equip Foundation and K.S.Rangasamy Institute for Engineering & Technology.
- Five days workshop on “Electronic Circuits II” conducted by Equip Foundation and K.S.Rangasamy Institute for Engineering & Technology.
- One week workshop on ‘Recent Trends in Signal Processing’ conducted by K.S.Rangasamy College of Technology (**AICTE Sponsored**).
- Two weeks workshop on ‘Signals & System’ **conducted by IIT, Khargpur**.
- Two days workshop on “Research Methodology in Signal Processing” conducted by Knowledge Institute of Technology, Salem.
- One day workshop on “MIC Design, Layout & Fabrication” in **NIT, Trichy**.
- One Day Seminar on “VLSI, Embedded & DSP Application” in Sona College of Technology.
- One day workshop on “PCB Design, Layout & Fabrication “in Anna University, Coimbatore.
- Two days programme on “Internal Audit – ISO 9001:2000” conducted by Bullseye Consulting Group, Bangalore.
- Fifteen days course on “PCB Designing” at Gaark Embedded Labs, Coimbatore.
- One week Pedagogy training at Sona College of Technology.
- One Day on RENESAS [RL78], in Government College of Engineering, Salem.

PUBLICATIONS

- **Prasad.B, Vinothini.A**, ‘Segmentation of Clusters Nuclei Based on Intensity and Texture in Phase Contrast Image using H-Maxima Transformation’ in IEEE International Conference on Wireless Communications, Signal Processing and Networking, WISPNET, 2016.
- **Prasad.B, Vinothini.A**, ‘H-Maxima Transformation based Image Segmentation of Clump Nuclei in Phase Contrast Image’ in 4th National Conference of NCICCT, 2016.
- **Prasad.B, Kanmani.T**, ‘Advanced Implementation of Traffic Control System’ in National Conference of NCCCTN, 2016.

- **Prasad.B, Praveenkumar,** ‘An Efficient 3D Statistical Shape Model Segmentation of the Fetal Cerebellum on Ultrasound Image’ in second International Conference on Engineering Technology and Science, ICETS – 15, 2015.
- **Prasad.B, Ramkumar. D,** ‘Multisource Power Generation and Wireless Monitoring System’ in First National Conference on Recent Trends in Communication and Information Technology, RTCIT – 14, March 2014.
- **Prasad.B, Lalithkumar V.N,** ‘Boundary Detection of Aorta in Noisy MR images using Edge Following Algorithm’ in Proceedings of the International Conference on Innovations in Information, Embedded and Communication Systems, Vol 1, pp – 498-502, 2014.
- **Prasad.B, Priyanga. GU,** ‘Active Noise Cancellation System Using DSP Processor, International Journal of Scientific and Engineering Research, (IJSER) Volume 4, Issued 4, ISSN (2229-5518), April 2013.
- **Prasad.B, Jayanthi. K.B** ‘Detection of Neural Tube and Central Nervous system Defects in Fetal Brain’, in Proceedings of the Fourth National Conference on Trends in communication, NCTC-10, 2010.
- **Prasad.B, Jayanthi. K.B,** ‘Abnormalities in Fetal Brain – A Review’ in Proceedings of the Fourth National Conference on ETCIT – 10.

PROPOSAL SUBMITTED

- Submitted a proposal under Minor Research project on the area ‘Computer Aided Detection of abnormalities in fetal images’ to **University Grants Commission**, 2015.
- Submitted a proposal on the area ‘Computer Aided Detection of abnormalities in fetal images’ to **Tamilnadu State Council for Science and Technology**, 2014.

BOOKS/MANUALS AUTHORED

- **MANUAL:** Electronic Circuit I & II lab, Linear Integrated Circuit Lab, Digital Electronics Lab, Electronic System Design Lab & PCB Lab

CERTIFICATION INVOLVEMENT

- Worked for ISO certification.
- Worked for NBA certification.

MEMBERSHIP

- Life member of IETE.
- Life member of ISTE.

PROJECT

➤ **M.E - AUTOMATIC DETECTION OF FETAL BRAIN ABNORMALITIES**

- The project is based on Medical Image Processing.
- It finds its application in field of identifying the brain abnormalities in fetus.
- The purpose of this project is to provide precision imaging of the abnormalities, which are not noticeably in ultrasound image.
- Since poor people are not very much interested in spending more money for MRI scanning.

➤ **B. E - MULTIZONE SAFEGUARD AND QUICK ALERTING SYSTEM**

- It provides Security to Banks, Apartments and areas where security is to be tightened.
- It includes Timing & Control Circuit, Siren Tone Generator, FM Transmitter & Receiver, Voice Record/Play Circuit & Telephone Interface.
- When Sensors are affected, then alert is by means of Speaker in Security Office & through mobile of the house owners.
- Alert is a recorded voice from Tape Media/Digital IC.

➤ **DIPLOMA - FORMATTED TERMINAL**

- It's a Language Converter based on GIST Technology.
- It is used in Railway Ticket Reservation, Land Lord Records and Super Markets.