YUVARANI M

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Pudur

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ACADEMIC EXPERIENCE

Name of the College	Designation	Joining	Relieving	Experience	
		Date	Date	Years	Months
Dhirajlal Gandhi College of Technology	Assistant Professor	01.06.2015	Till date	1	8
Total				1 year	8 months

EDUCATIONAL QUALIFICATION

P.G: M.E. (Applied Electronics)

Institution : Aksheyaa College of Engineering, Maduranthagam.

University : Anna University, Chennai.

Duration : 2012 - 2014

Aggregate : 87.2% (University 18th Rank)

U.G : B.E. (Electronics and Communication Engineering)

Institution : Vidhyaa Vikas College of Engineering and Technology, Tiruchengode.

University : Anna University, Chennai.

Duration : 2008 - 2012

Aggregate : 88.9% (First Class with Distinction)

HSC

Institution : Government Higher Secondary School, Dharapuram.

Board : State Board
Duration : 2007 - 2008
Aggregate : 80.58%

SSLC

Institution : Government Higher Secondary School, Dharapuram.

Board : State Board
Duration : 2005 - 2006
Aggregate : 83.20%

PROGRAMMING PROFICIENCY

Languages : HDL, Embedded C, Basics of C and Java

Assembly languages : Microprocessor (8085, 8086), Microcontroller (8051)

Packages : Modelsim SE 5.5e, MATLAB

COURSE ATTENDED

Basic Electronics & Circuit Designing.

Embedded Systems & Designing

Wireless Networks and its Applications

PROJECTS

Title : Remote Controlled diesel Generator

Platform : Embedded system

Description : Controlling the diesel generator in vehicles by using Zapper.

Title : Cryptanalysis of Remote User Authentication Scheme Using Smart Card

Platform : Embedded system

Description : Secured accessing is obtained by authentication schemes

Title : Shrug and Voice Recognition System for Dumb and Hearing Impards

Platform : Digital Image Processing

Description : providing communication interface for the deaf and mute communities to the

Society.

JOURNALS

Published a paper in International Science and Research Journal in the title of TUMOR IDENTIFICATION IN BRAIN MR IMAGES USING DIGITAL IMAGE PROCESSING BASED ALGORITHM.

Published a paper in IJRAT Journal in the title of SHRUG AND VOICE RECOGNITION SYSTEM FOR DUMB AND HEARING IMPARDS.

FDP ATTENDED:

Participated FDP on Innovative methods in Teaching in DGCT.

Attended FDP on "Demystifying Electronic System Design and Engineering" organized by ICET, Pedagogy Solutions Engineering in DGCT.

Attended FDP on "Embedded & Real time Systems" organized by Vellalar Engineering College, Erode.

Attended FDP on "Embedded and Real Time Systems" organized by St.Joseph's Institute of Technology, Chennai.

CONFERENCES

International conference on COMPETENCY BASED TEACHER EDUCATION held at Vidhyaa Vikas College of Engineering and Technology, Tiruchengode.

Presented a paper in International Conference on Futuristic Trends in Electronics Engineering in the title of HYBRID ENERGY MANAGEMENT SYSTEM BASED ON FUZZY LOGIC CONTROLLER FOR POWER DISTRIBUTION held at Thiruvalluvar College of Engineering and Technology, Vandhavasi.

Presented a paper in National Conference PCT'13 on Power on communication and technology in the title of EFFICIENT REMOVAL OF IMPULSE NOISE BY A LOW COST VLSI IMPLEMENTATION held at Aksheyaa College of Engineering, Maduranthagam.

Presented a paper in National Conference NCACSP'13 on Advanced Communication and Signal Processing in the title of SHRUG AND VOICE RECOGNITION SYSTEM FOR DUMB AND HEARING IMPARDS held at Arulmigu Meenakshi Amman College of Engineering, Kancheepuram.

Presented a paper in National Conference RIECE on Recent Innovations in Electronics and Communication Engineering in the title of CUES AND SPEECH CONVERSION SYSTEM FOR DEAF AND DUMB PEOPLE held at Meenakshi Sundararajan Engineering College, Kodambakkam.

Presented a paper in National Conference on EMERGING ELECTRONICS'14 in the title of AN AUTOMATIC IDENTIFICATION OF NAME, LOCATION, SIZE OF TUMOR IN BRAIN MR IMAGES held at Chendu College of Engineering and Technology, Zamin Endathur.

Presented a paper in National Conference NCETCS 2016 on NATIONAL CONFERENCE ON "EMERGING TRENDS IN COMMUNICATION SYSTEMS" in the theme "Digital India" in the title of GRAPHICAL USER INTERFACE BASED DIAGNOSTIC DECISION SUPPORT SYSTEM FOR PRENATAL SPINA BIFIDA held at Dhirajlal Gandhi College of Technology, Salem.

WORKSHOPS

Attended Workshop on HANDS ON TRAINING IN ROBOTICS held at Vidhyaa Vikas College of Engineering and Technology, Tiruchengode.

Attended Workshop on MEASUREMENT BASICS & RF BASICS held at Vi Institute of Technology, Sirungundram.

SEMINARS

Attended Seminar on DGITAL COMMUNICATION AND ITS APPLICATONS held at Vidhyaa Vikas College of Engineering and Technology, Tiruchengode.

Attended Seminar on SMALL SYSTEM DESIGNING OF ANTENNA held at Kongu Engineering College, Perundurai.

EXPO

Participated in the PROJECT EXPO 2K12 with the project titled Cryptanalysis of Remote User Authentication Scheme Using Smart Card

ROLES AND RESPONSIBILITIES

Class Representative in college and school.

Student member of Indian Society of Electronics and Communication Engineers.

Certified in BULATS by the Association of Language Testers in Europe.

PERSONAL PROFILE

Date of Birth : 4th May 1991 Age : 22 Years

Father's Name : Mr.T.V.Mohan

Languages Known : English, Tamil and Telugu

Marital status : Unmarried
Gender : Female
Nationality : Indian

Hereby, I declare that all the details said above are true to the best of my knowledge.

Place: Salem
Date: [YUVARANI M]