

N.VETRISELVAN, M.E,

Department of Electronics and Communication Engineering
Dhirajlal Gandhi College of Technology
Opp.To Salem Airport
Salem-636 309



Academic Background

College/University	Degree	Specialization	Year
Hindusthan College of Engineering and Technology	B.E	ECE	2010
Sona college of technology	M.E	VLSI Design	2013

Academic Experience

Institute	Designation	Duration
Dhirajlal Gandhi College of Technology, Salem	Assistant Professor	2013-till date

Area of Interest

- VLSI Design
- Digital Electronics
- Electric Circuits

Courses taught

- VLSI Design
- Digital Electronics
- Digital Communication
- Wireless Communication
- Digital Communication and Receivers
- Embedded and Real Time Systems
- Circuit Theory
- Linear Integrated Circuits

Membership

- Indian Society For Technical Education (ISTE)

Journal Publication

- **N.Vetriselvan,P.N.Palanisamy**, “A Coarse grained recovery boosting technique to enhance NBTI in SRAM array “ International Journal of Innovative Research in Computer and Communication Engineering, Volume 4,Issue 2, February 2016.

Participated in National conferences

- Presented a paper titled “**VLSI Implementation of scalable parallel architecture for multilevel lifting 2-D DWT**” in the National conference on emerging trends in communication systems held on 2nd April,2016 .

Online course

- NPTEL Course – **Digital Circuits (Top 5% in all India) with 83% - Elite Certification**

Proposal Submitted

- Submitted a Proposal for Faculty development training programme on “**EC6601- VLSI Design** “in the center for Faculty development, Anna university, Chennai.

Seminars/ Workshops/ FDTP Attended

- Attended a one day workshop on Smart Irrigation System using Intelligent Techniques held on 5th February 2020 at Sona college of technology.
- Visited to Department of Electrical Engineering, IIT Madras conducted by PALS on 10.11.16.
- Participated in FDTP on “Demystifying Electronic system level design and engineering” organized by competency in ICET from 16.06.16 to 18.06.16.
- Attended a two days workshop on low power ASIC chip design using mentor Graphics EDA tools held on 3rd and 4th march 2016 at Sona college of technology.

- Attended NI Days 2015 held on November 19,2015 at Leela palace Bangalore.
- Participated in Anna University sponsored FDTP on EC6201- Electronic Devices at Mahendra college of engineering from 23rd to 29th November 2015.
- Visited Tessolve semiconductor Pvt limited ,Bangalore on October 2015.
- Attended a Seminar on Cadence, Orcad, PSPICE and PCB at BMS College of Engineering, Bangalore, held on 31/07/2015.
- Workshop on Advanced design System organized by Dhirajlal Gandhi College of Technology, Salem on 07.02.2015.
- Participated in the Faculty Development Program on "Personal Effectiveness to Organisational Excellence" Jan 10 2015 at CII-Education & Skill panel and KBSS,Salem.
- Faculty Development Programme on "Texas Instruments Analog System Design, Embedded System Using MSP430" at Dhirajlal Gandhi College of Technology, Salem on 08 & 09.05.14 .
- Attended workshop on "VLSI Labs Solution using Tanner EDA and mentor Graphics tools" Aug 16-17 2013 at Government College of Engineering,Salem.

Other Activities

- Class Advisor
- Organizing committee member for various departmental & institutional activities
- Guiding Students projects