

SATHYA.E

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CAREER OBJECTIVE:

To work with a firm with a professional work driven environment where I can utilize and apply my knowledge, skills which would enable me as a fresh graduate to grow while fulfilling organizational goals.

EDUCATIONAL QUALIFICATIONS:

COURSE	YEAR OF PASSING	NAME OF INSTIUTION	AGGREGATE
M.E (Power Electronics & Drives)	May 2018	Government College of Technology, Coimbatore	91.1%
B.E (Electrical & Electronics Engineering)	April 2016	S.K.P Engineering College, Thiruvannamalai	81.1%

EXPERIENCE:

- **1 year and 9 months** of experience in teaching as Assistant Professor, Dhirajalal Gandhi College of Technology,

SUBJECTS HANDLED:

Salem.

- Electrical Machines-I
- Power Quality
- Circuit theory
- Electromagnetic theory
- Basic electrical and instrumentation engineering
- Electrical Machines-II , Electric circuits and control system laboratory

SOFTWARE PROFICIENCY:

- MATLAB
- Xilinx
- RT-Lab
- C basics

ACADEMIC AREA OF INTEREST:

- Power Electronics & Drives
- Electrical Machines
- Circuit Theory

INDUSTRIAL EXPOSURE:

UNDERWENT INPLANT TRAINING IN:

- **TNEB 110/33KV Substation**, Samuthiram, Thiruvannamalai.

UNDERGONE WORKSHOPS ON:

- Two day workshop on **Simulation of IEEE Transaction papers on Industrial Electronics** Conducted by Dr. Mahalingam College of Engineering and Technology, Coimbatore.
- One day workshop on **Android Application Development Program** Conducted by
- Member in IEEE

UNDERGONE FACULTY DEVELOPMENT PROGRAMME ON:

- **Solar PV and Wind Energy System Design and Applications with Simulation Software** at Government College of Technology, Coimbatore, Sep 2016.
- **Smart Grid and Smart City** at Government College of Technology, Coimbatore, Nov 2016.

UNDERGONE HANDS ON TRAINING PROGRAMME ON:

- **FPGA controller for Power Converters** at Government College of Technology, Coimbatore, Feb 2017.
- **MATLAB for Renewable Energy Research Issues** at Government College of Technology, Coimbatore, Feb 2017.
- **Real Time Simulator** at Government College of Technology, Coimbatore, March 2017.

PAPER PRESENTED:

- Paper titled **Enhancement of low voltage ride through capability for PMSG based wind energy conversion system with super capacitor** has been presented at 4th international conference on electrical energy systems on February 2018.
- Paper titled **Selective Harmonic Elimination in Symmetrical Cascaded H-Bridge Multilevel Inverter with Newton Raphson Algorithm** has been presented at National Conference on Alternate Energy Technologies on April 2018.

PAPER PUBLISHED:

- Paper titled **Enhancement of low voltage ride through capability for PMSG based wind energy conversion system with super capacitor** has been published in IEEE Explore 23 Aug 2018.
- Paper titled **Selective Harmonic Elimination in Symmetrical Cascaded H-Bridge Multilevel Inverter with Newton Raphson Algorithm** has been published in SSEE DigiBook May 2018.

AWARDS & ACHIEVEMENTS:

- Got Second Prize in B.E for Technical Quiz during the academic year, 2016 to 2017.
- Got Second Prize in B.E for Group Discussion during the academic year, 2015 to 2016.
- Got Second Rank in M.E for the academic year, First sem 2016 to 2017.
- Got Second Rank in M.E for the academic year, Second sem 2016 to 2017.
- Completed the course of Diploma in MS Office, 2008.

PROJECT DETAILS:

M.E PROJECT:

Phase I-Project Title: Enhancement of low voltage ride through capability for PMSG based wind energy conversion system With Super Capacitor.

Description: The characteristic of LVRT is found to be enhanced with super capacitor when compare to Chopper resistor for PMSG based wind energy conversion system.

Phase II-Project Title: Design, Simulation and Realization of FPGA based high performance Cascaded Nine level Inverter with Selective Harmonic Elimination Technique.

Description: The performance of nine level cascaded inverter is found to be enhanced with Selective Harmonic elimination Technique based on the Switching Angles determined from Newton Raphson Algorithm.

B.E PROJECT:

Project Title: Hysteresis current control based minimization of torque ripple in Switched Reluctance Motor.

Description: The hysteresis PWM current controller with linearized SRM model is used to run the SR motor speed as constant and to get the reduced torque ripple.

PROJECT GUIDED: Fuzzy Logic based Two-stage matrix converter and Harmonic Elimination to improve power Quality by using DPFC.

